

Editorial

The present special issue of the Estonian Journal of Engineering continues the tradition of publishing selected papers of the biennial IEEE International Baltic Electronics Conferences – BEC (the BEC2008 in this case, see the foreword by Prof. Toomas Rang). Previous seven special issues were published in 1997, 2000, 2001, 2004, 2006 and 2007 in the series “Engineering” of the Proceedings of the Estonian Academy of Sciences.



This time, seven scientific papers have been selected. At least one co-author of each paper belongs to the new entity CEBE at Tallinn University of Technology – the Centre of Research Excellence for Integrated Electronics Systems and Biomedical Electronics, founded in 2008 (see the overview of CEBE by Prof. Raimund Ubar). The first paper by V. Voitovich, T. Rang and G. Rang analyses technologies for GaAs power diode structures, and the second one by M. Tagel, P. Ellervee and G. Jervan considers system level methods for the design of real-time systems-on-chip. The next two papers, by A. Karputkin, R. Ubar, J. Raik and M. Tombak, and by M. Jenihhin, J. Raik and R. Ubar, describe high-level decision diagrams for representing digital electronic systems and their verification on the basis of these diagrams. The last three papers by authors from Technomedicum of the TUT are devoted to biomedical physics and technology and also to the processing of biomedical signals.

I would like to express my thanks to the contributing authors and people who made this issue available in the present form. I wish also a great success to the next, already 12th Baltic Electronics Conference BEC2010, which will be held in October 2010 in Tallinn.

Mart MIN

*Professor of Electronic Measurements at Tallinn University of Technology,
Member of the steering committee of the Centre for Integrated Electronic Systems and
Biomedical Engineering (CEBE, Estonian centre of excellence in research),
Vice-Chair of BEC2008*